

PTO-1449

Application No.

09/870,944

Applicant(s)

Michael I. Catherwood

Docket Number

068354.1443

Group Art Unit

2124

Filing Date

June 1, 2001

Information Disclosure Citation
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	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
CD	3930253	12/30/75	Malda	340	347	1/24/74	
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C.	4615005	9/30/86	Maejima et al.	713	601	7/20/84	
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F.	4800524	1/24/89	Roesgen	364	900	12/2/87	
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J.	5239654	8/24/93	Ing-Simmons et al.	395	800	11/17/89	
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CD	L.	5392435	2/21/95	Masui et al.	395	725	9/29/93

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CD	T.	Intel, Pentium Processor Family Developer's Manual, Volume 3: Architecture and Programming Manual, , Pages 3-1, 3-2, 3-15, 14-1 to 14-30, 18-7, and 25-289 to 25-292	1995
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	F. 5564028	10/8/96	Swoboda et al.	395	375	1/11/94
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	L. 5748970	5/5/98	Miyaji et al.	395	733	4/30/96
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	N. 5778237	7/7/98	Yamamoto et al.	713	322	12/14/95
	O. 5812868	9/22/98	Moyer et al.	395	800.23	9/16/96
	P. 5815693	9/29/98	McDermott et al.	713	501	12/15/95
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(D)	X.	SPARC, International, Inc., "The SPARC Architecture Manual", Version 9, pp. 137-299	1994-2000
(D)	Y.	Free On-Line Dictionary of Computing (FOLDOC), http://wombat.doc.ic.ac.uk/foldoc/foldoc.cgi?query=program+counter	1995

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	Docket Number	Group Art Unit	Filing Date
	068354.1443	2124	June 1, 2001

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E. 5974549	10/26/99	Golan	713	200	3/27/97
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R. 6282637	8/28/01	Chan et al.	712	223	12/2/98
S. 6292866	9/18/01	Zaiki et al.	710	264	3/30/99
T. 6315200	11/13/01	Silverbrook et al.	235	454	7/10/98

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PTO-144

Application No.

09/870,944

Applicant(s)

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Group Art Unit

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D.	2003/0093656	5/15/03	Masse et al.	712	241	10/1/99
E.	6658578	12/2/03	Laurenti et al.	713	324	10/1/99
F.	6681280	1/20/04	Miyake et al.	710	261	10/4/00
G.	6728856	4/27/04	Grosbach et al.	711	202	6/1/01
H.	6763478	7/13/04	Vinh X. Bui	713	600	10/24/00
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CD	D.	5623646	4/22/97	Clarke	395	560	9/12/95
CD	E.	4709324	11/24/87	Kloker	364	200	11/27/85
CD	F.	5561384	10/1/96	Reents et al.	327	108	11/8/95
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